# EAST Search History

| Ref # | Hits | Search Query | DBs | Default Operator | Plurals | Time Stamp |
|---|---|---|---|---|---|---|
| S1 | 128 | 703/19.ccor. | US-PGPUB; USPAT | OR | ON | 2006/09/23 14:35 |
| S2 | 392 | 703/13.ccor. | US-PGPUB; USPAT | OR | ON | 2006/09/23 14:35 |
| S3 | 632 | 703/14.ccor. | US-PGPUB; USPAT | OR | ON | 2006/09/23 14:36 |
| S4 | 459 | 703/22.ccor. | US-PGPUB; USPAT | OR | ON | 2006/09/23 14:48 |
| S5 | 76 | 703/20.ccor. | US-PGPUB; USPAT | OR | ON | 2006/09/23 14:48 |
| S6 | 13138 | simulat$4 same hardware | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2006/09/23 16:10 |
| S7 | 1333 | S6 and suspend$3 | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2006/09/23 16:11 |
| S8 | 452 | S7 and threshold | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2006/09/23 16:13 |
| S9 | 105 | S8 and restart | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2006/09/23 16:15 |
| S10 | 120406 | execut$3 near3 time | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2006/09/23 16:17 |
| S11 | 51 | S9 and S10 | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2006/09/23 16:17 |
| S12 | 47 | S11 and synchron$5 | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2006/09/23 16:19 |
| S13 | 37 | S12 and accumulat$3 | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2006/09/23 16:20 |
| S14 | 32 | S13 and tag | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2006/09/23 16:21 |
| S15 | 26 | S14 and @ad<="20040419" | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2006/09/23 16:46 |
| S16 | 5553 | (real adj time) with simulat$4 | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2006/09/23 16:47 |
| S17 | 432 | S16 and suspend$3 | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2006/09/23 16:47 |
| S18 | 74 | S17 and restart | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2006/09/23 16:48 |

| S19 | 55 | S10 and S18 | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2006/09/23 16:48 |
|-----|-----|-------------|------------------------------------|-----|-----|-------------------|
| S20 | 44 | S19 and synchron$5 | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2006/09/23 16:49 |
| S21 | 42 | S20 and @ad<="20040419" | US-PGPUB; USPAT; EPO; JPO; DERWENT | OR | ON | 2006/09/23 16:50 |
| S22 | 47 | ("5025364" | "5493508" | "5493672" | "5515525" | "5546562" | "5590049" | "5600579" | "5600790" | "5623418" | "5663900" | "5664098" | "5673418" | "5675771" | "5678028" | "5768567" | "5771370" | "5787245" | "5801958" | "5809450" | "5812431" | "5815715" | "5819065" | "5838948" | "5848236" | "5848270" | "5857091" | "5862361" | "5867399" | "5867400" | "5870308" | "5870585" | "5870588" | "5872958" | "5886899" | "5909578" | "5913052" | "5918035" | "5943490" | "5946472" | "5960181" | "5960182" | "5963724" | "6009256" | "6202044" | "6212489" | "6389379" | "6389382").PN. | US-PGPUB; USPAT; USOCR | OR | ON | 2006/09/23 16:59 |
| S23 | 3 | ("6584436").URPN. | USPAT | OR | ON | 2006/09/23 17:49 |

| | | Results |
|---|---|---|
| 11. | (((((pub-date > 1959 and pub-date < 2005 and FULL-TEXT(simulat!) and FULL-TEXT(timing control)) and synchron!) and threshold) and hardware) and execution time<br>*[All Sources(- All Sciences -)]* | 3 |
| 10. | ((((pub-date > 1959 and pub-date < 2005 and FULL-TEXT(simulat!) and FULL-TEXT(timing control)) and synchron!) and threshold) and hardware<br>*[All Sources(- All Sciences -)]* | 32 |
| 9. | (((pub-date > 1959 and pub-date < 2005 and FULL-TEXT(simulat!) and FULL-TEXT(timing control)) and synchron!) and threshold<br>*[All Sources(- All Sciences -)]* | 61 |
| 8. | (pub-date > 1959 and pub-date < 2005 and FULL-TEXT(simulat!) and FULL-TEXT(timing control)) and synchron!<br>*[All Sources(- All Sciences -)]* | 161 |
| 7. | (pub-date > 1959 and pub-date < 2005 and FULL-TEXT(simulat!) and FULL-TEXT(timing control)) and suspend<br>*[All Sources(- All Sciences -)]* | 5 |
| 6. | pub-date > 1959 and pub-date < 2005 and FULL-TEXT(simulat!) and FULL-TEXT(timing control)<br>*[All Sources(- All Sciences -)]* | 374 |
| 5. | (((((pub-date > 1959 and pub-date < 2005 and FULL-TEXT(simulat!) and FULL-TEXT(suspend)) and hardware) and restart) and timing) and threshold<br>*[All Sources(- All Sciences -)]* | 4 |
| 4. | (((pub-date > 1959 and pub-date < 2005 and FULL-TEXT(simulat!) and FULL-TEXT(suspend)) and hardware) and restart) and timing<br>*[All Sources(- All Sciences -)]* | 21 |
| 3. | ((pub-date > 1959 and pub-date < 2005 and FULL-TEXT(simulat!) and FULL-TEXT(suspend)) and hardware) and restart<br>*[All Sources(- All Sciences -)]* | 59 |
| 2. | (pub-date > 1959 and pub-date < 2005 and FULL-TEXT(simulat!) and FULL-TEXT(suspend)) and hardware<br>*[All Sources(- All Sciences -)]* | 378 |
| 1. | pub-date > 1959 and pub-date < 2005 and FULL-TEXT(simulat!) and FULL-TEXT(suspend)<br>*[All Sources(- All Sciences -)]* | 1741 |

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|---|---|---|
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| #2 | ((simulat*<and>suspend<and>restart)<and>threshold) <and> (pyr >= 1951 <and> pyr <= 2004) | 108 |
| #3 | ((simulat*<and>suspend<and>restart) <and>threshold<and>synchron*) <and> (pyr >= 1951 <and> pyr <= 2004) | 75 |
| #4 | ((simulat*<and>suspend<and>restart) <and>threshold<and>synchron*<and>accumulat*) <and> (pyr >= 1951 <and> pyr <= 2004) | 39 |
| #5 | ((simulat*<and>suspend<and>restart) <and>threshold<and>synchron*<and>accumulat*<and>tag) <and> (pyr >= 1951 <and> pyr <= 2004) | 26 |

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Evaluating the Impact of Coherence Protocols on Parallel.. - Costa, Bianchini, Dutra (1996)  (Correct)  (1 citation)
benefits. In this paper we use execution-driven **simulation** of a scalable multiprocessor to evaluate the
As an example, because of the high cost of **suspend**ing and **restart**ing processors, it is very common
because of the high cost of **suspend**ing and **restart**ing processors, it is very common that idle
www.cos.ufrj.br/pub/tech_reps/es38996.ps.gz

IDRA (IDeal Resource Allocation): Computing Ideal.. - Fern'andez Carro..  (Correct)
Keywords: Parallel Logic Programming **Simulation** Parallelism Concurrency Performance
Fig. 1. And-Parallel Execution Start_goal Fork **Suspend Restart** A B B P P P Q Q Start_goal
And-Parallel Execution Start_goal Fork **Suspend Restart** A B B P P P Q Q Start_goal Finish_goal Fig. 2.
www.clip.dia.fi.upm.es/~clip/papers/idra-europar96.ps.gz

Modeling Queueing Systems Using Hierarchical Control Flow Graph.. - Sargent (1997)  (Correct)
Control Flow Graph Models Robert G. Sargent **Simulation** Research Group, 439 Link Hall, Syracuse
It has three input ports: new-jobs"**suspend**-operation"and "**restart-suspend**ed-job"and
ports: new-jobs"**suspend**-operation"and "**restart-suspend**ed-job"and one output port:
erc.cat.syr.edu/srg/HCFG-QueueingModels.ps.gz

An Overview Of Hierarchical Control Flow Graph Models - Fritz, Sargent (1995)  (Correct)
Graph Models Douglas G. Fritz Robert G. Sargent **Simulation** Research Group Syracuse University 439 Link
"It has three input ports: new-jobs"**suspend**-operation"and "**restart-suspend**ed-job"and
ports: new-jobs"**suspend**-operation"and "**restart-suspend**ed-job"and one output port:
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of sequential or parallel execution and **simulation**, and the algorithms that allow implementing the
Start_goal Start_execution Fork Finish_goal **Suspend Restart** Figure 2: Or-Parallel Execution Node
Start_execution Fork Finish_goal **Suspend Restart** Figure 2: Or-Parallel Execution Node Comment
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... a test vector and referring to **simulation** results after ... conflicts due to software,
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S Matthews-Frederick - Department of Information Technology and Electrical ..., 2004 - innovexpo.itee.uq.edu.au
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**hardware** design. ... microcontroller designed for **timing control**. ...
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... The proper **simulation** of the microprocessor chip is very ... from the PCB to the respective
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SB House - 1998 - hegel.ittc.ku.edu
... 3.1 Proportional Time Thread **Suspend**-Work Loop . . . . . ... access and modest **hardware**
cost. ... **simulation**, including the alternative solutions already mentioned. ...
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V Lohweg, C Diederichs, D MÜLLER - EURASIP Journal on Applied Signal Processing, 2004 - hindawi.com
... Algorithms for **Hardware**-Based Pattern Recognition ... Also Eichhorn [9] and Page
2. Algorithms for **Hardware**-Based Pattern Recognition 1913 ...
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S Ghosh - 2003 - books.google.com
... with the socially accepted, strict, legal **threshold** implied in ... problems ranges from
distributed discrete- event **simulation** of **hardware** description models in ...
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**The Link-Board Control in the RPC Trigger System for the CMS Experiment**
D Ungaro - HIP, 2004 - ethesis.helsinki.fi
... as during the test, calibration and standby periods, and ... The **hardware** and software
tools needed for the ... experiments have been approved to start their operation ...
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T de Doctorat - 2003 - lphe.epfl.ch
... 31 3.2.2.3 Digital repeater card and overall **timing/control** layout ... Therefore, the
**threshold** values for the pt triggers can be set low for electrons, muons and ...
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J Li - 2004 - engr.oregonstate.edu
... 7 4.1 **Simulation** results of CMOS inverter ... 4 power-up or standby, it is desirable
to run the ... The minimal addition of analog **hardware** for calibration keeps the ...
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JW Lockwood - 1996 - arl.wustl.edu
... 119 C.3 **Timing control** (timingctrl). ... are provided. A **simulation** shows that the iMCRA
provides near-optimal performance using only minimal **hardware**. ...

[PS] AN ARCHITECTURE FOR END-TO-END QUALITY OF SERVICE PROVISION AND ITS EXPERIMENTAL VALIDATION - group of 3 »
K Nahrstedt - 1995 - cis.upenn.edu
... due to resid- ing in a kernel, where they have access to real-time services
of the OS, and (3) **hardware** support with better timer ...
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Operating System Support for Low-Latency Streaming - group of 4 »
A Goel - 2003 - eecg.toronto.edu
... Dedication To my parents, whose wholehearted support helped me start this endeavor. ...
5.8 Square wave **simulation** . . . ... normally reserved for dedicated **hardware**. ...
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LLE Review, Volume 57. Quarterly report, October--December 1993
A Simon - 1993 - osti.gov
... About the Cover: A remote camera **suspended** from the OMEGA ... only when the TPI is well
above its **threshold**. ... dimensional **simulation**, an initial single-wavelength ...
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[BOOK] Interactive Distributed Multimedia Systems and Telecommunication Services: 6 th International ...
M Diaz, P Owezarski, P Sénac - 1999 - books.google.com
... of the QoS Required by a Distributed Inter -active **Simulation** Application in ... 2. **Hardware**
Acceleration PCI Card Page 18. ... 8. Start the FPGAs using control register ...
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" The Artilect War" First Draft Prof. Dr. Hugo de Garis
S Draft - Update, 1999 - cs.usu.edu
... By programming another piece of electronic **hardware** which measures ... But, if we start
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Dirk Grunwald, Soraya Ghiasi
November 2002 **Proceedings of the 35th annual ACM/IEEE international symposium on Microarchitecture**
**Publisher:** IEEE Computer Society Press
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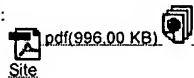
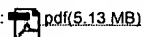**2** Special section: Reasoning about structure, behavior and function
B. Chandrasekaran, Rob Milne
July 1985 **ACM SIGART Bulletin,** Issue 93
**Publisher:** ACM Press
Full text available: pdf(5.13 MB)  Additional Information: full citation, abstract, references

**3** Experience Using Multiprocessor Systems—A Status Report
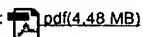Anita K. Jones, Peter Schwarz
June 1980 **ACM Computing Surveys (CSUR),** Volume 12 Issue 2
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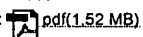**4** Characterizing the caching and synchronization performance of a multiprocessor operating system
Josep Torrellas, Anoop Gupta, John Hennessy
September 1992 **ACM SIGPLAN Notices , Proceedings of the fifth international conference on Architectural support for programming languages and operating systems ASPLOS-V,** Volume 27 Issue 9
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Full text available: pdf(1.52 MB)  Additional Information: full citation, references, citings, index terms
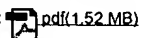
**5** Logged virtual memory
D. R. Cheriton, K. J. Duda
December 1995 **ACM SIGOPS Operating Systems Review , Proceedings of the fifteenth ACM symposium on Operating systems principles SOSP '95,** Volume 29 Issue 5
**Publisher:** ACM Press
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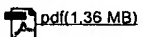Vincenzo Piuri
June 1990 **Proceedings of the 3rd international conference on Industrial and engineering applications of artificial intelligence and expert systems - Volume 2 IEA/AIE '90**
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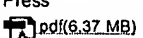**7** Computational strategies for object recognition
Paul Suetens, Pascal Fua, Andrew J. Hanson
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Allen M. Johnson, Miroslaw Malek
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9   The J-machine multicomputer: an architectural evaluation
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10  *AdJava*: automatic distribution of Java applications
Mohammad M. Fuad, Michael J. Oudshoorn
January 2002   **Australian Computer Science Communications , Proceedings of the twenty-fifth Australasian conference on Computer science - Volume 4 ACSC '02**, Volume 24 Issue 1
**Publisher:** Australian Computer Society, Inc., IEEE Computer Society Press
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11  VC-1: a scalable graphics computer with virtual local frame buffers
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August 1996   **Proceedings of the 23rd annual conference on Computer graphics and interactive techniques**
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12  Mostly lock-free malloc
Dave Dice, Alex Garthwaite
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13  Tuning high-performance scientific codes: the use of performance models to control resource usage during data migration and I/O
Jonghyun Lee, Marianne Winslett, Xiaosong Ma, Shengke Yu
June 2001    **Proceedings of the 15th international conference on Supercomputing**
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14  Run-time support for distributed sharing in safe languages
Y. Charlie Hu, Weimin Yu, Alan Cox, Dan Wallach, Willy Zwaenepoel
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15  A distributed, operating system based, blackboard architecture for real-time control
Daniel L. Larner
June 1990    **Proceedings of the 3rd international conference on Industrial and engineering applications of artificial intelligence and expert systems - Volume 1 IEA/AIE '90**
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